

ABSTRACT OF THE DISCLOSURE

A semiconductor device for efficiently transferring data of both digital and analog signals. The semiconductor
5 device includes a digital interface for controlling input and output of a digital signal, an analog interface for controlling input and output of an analog signal, and a transfer controller. The transfer controller controls the transfer of data between the digital interface and the
10 memory and between the analog interface and the memory. The data transferred between the analog interface and the memory is divided into plural pieces of data having a predetermined data length and converted to pseudo data packets.